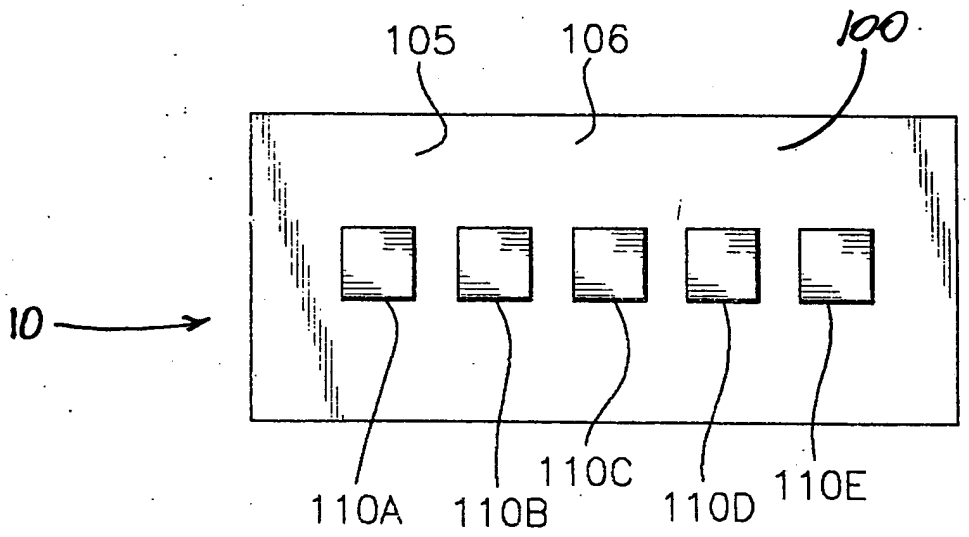

Fig. 1

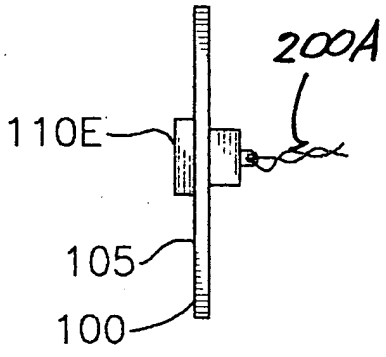

Fig. 2

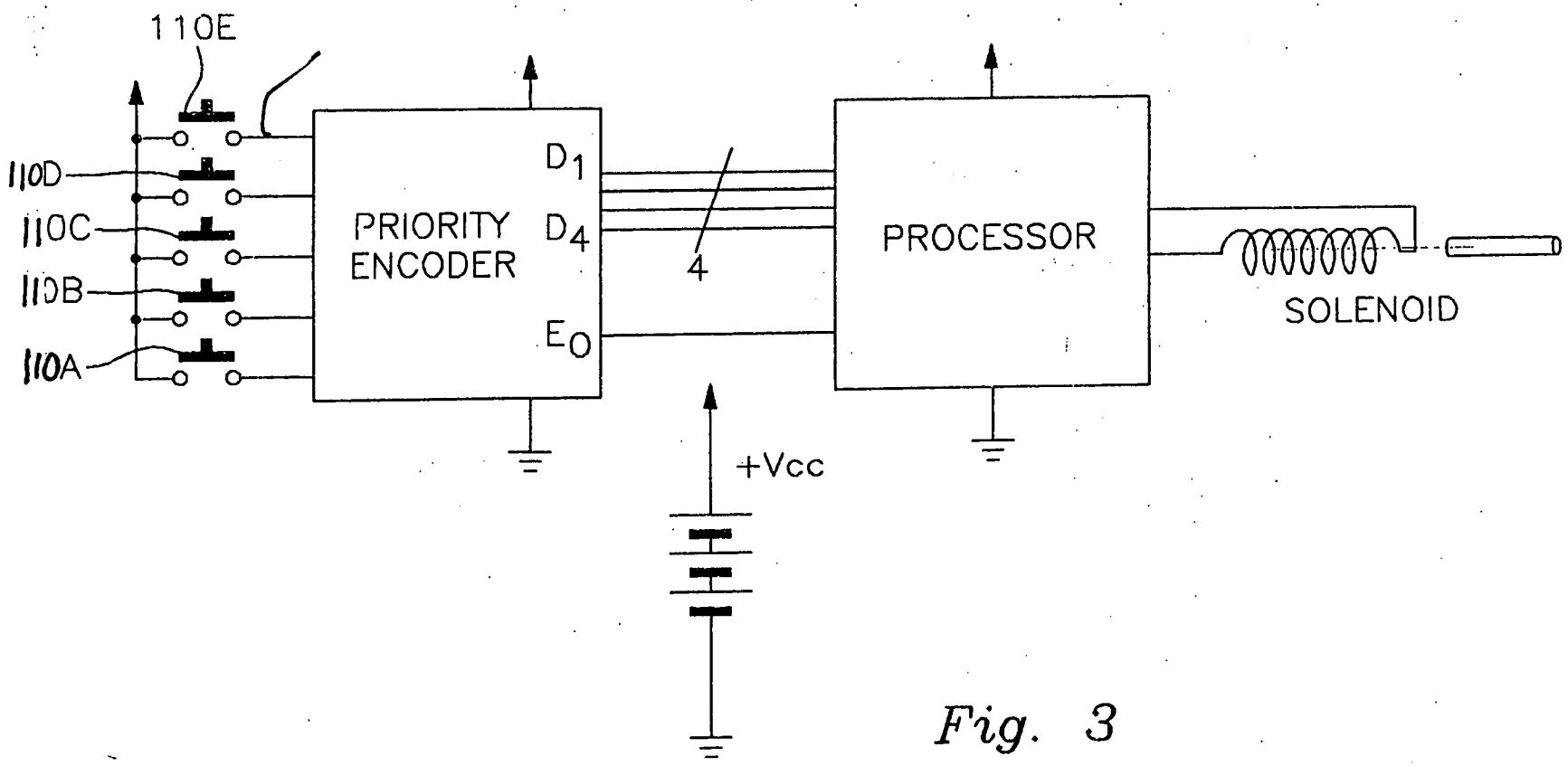
110E
110D
110C
110B
110A
PRIORITY ENCODER
D1
D4
E0
4
PROCESSOR
SOLENOID
+Vcc

Fig. 3

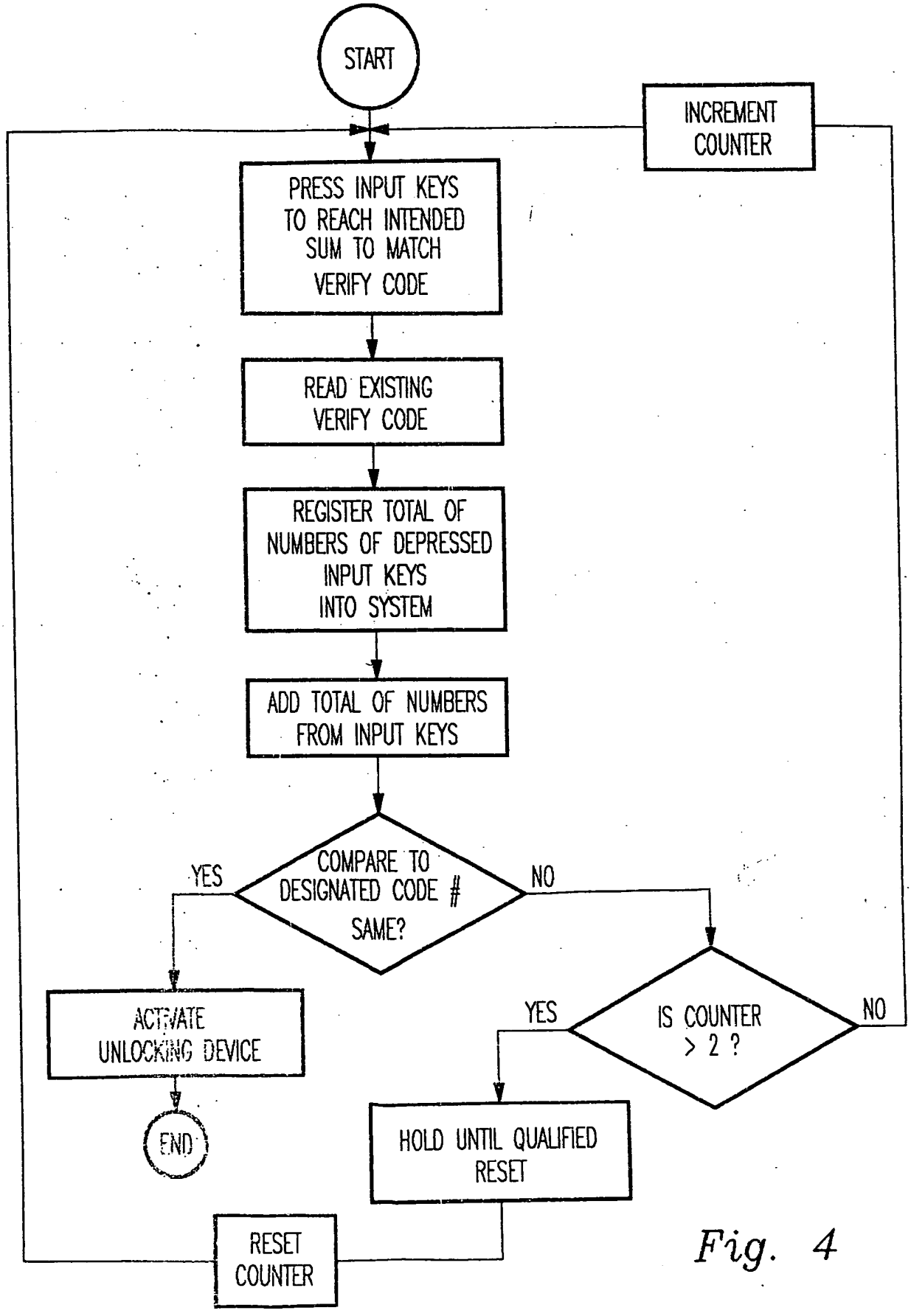
START
INCREMENT COUNTER
PRESS INPUT KEYS TO REACH INTENDED SUM TO MATCH VERIFY CODE
READ EXISTING VERIFY CODE
REGISTER TOTAL OF NUMBERS OF DEPRESSED INPUT KEYS INTO SYSTEM
ADD TOTAL OF NUMBERS FROM INPUT KEYS
COMPARE TO DESIGNATED CODE # SAME?
YES
NO
ACTIVATE UNLOCKING DEVICE
END
IS COUNTER > 2 ?
YES
NO
HOLD UNTIL QUALIFIED RESET
RESET COUNTER

Fig. 4